

REMARKS

Claims 1-2 and 4-7 were rejected as unpatentable over YASUI et al. 5,248,963 in view of KANEDA JP 11271715. Claims 1 and 7 have been amended and reconsideration and withdrawal of the rejection are respectfully requested.

A machine-generated English translation of KANEDA is in the Appendix.

Neither YASUI et al. nor KANEDA discloses or suggests that both of (1) the time during which all the gate lines are active, and (2) the time during which the same electric potential as applied to the common electrode is applied to all the data lines, are determined based on a time constant of a resistor and a capacitor.

Although the Official Action asserts that "Yasui teaches the predetermined time is determined based on a time constant of a resistor and a capacitor", the feature of determining the above (1) and (2) based on a time constant of a resistor and a capacitor, is NOT disclosed in YASUI et al. or KANEDA.

A device embodying the amended claims 1 and 7 of the present invention can obtain an effect that "even a simple circuit can adjust the time for driving the gate lines and the data lines after no signal has been input and can easily change the setting values" (specification of the present application,

page 7, lines 21-23). YASUI et al. and KANEDA cannot obtain such an effect because the feature described above is NOT disclosed or suggested therein.

New claims 8-13 are believed to be allowable because the references do not disclose or suggest a signal absence detector that detects that at least one of a video signal and a vertical synchronization signal is no longer being input to the liquid crystal display device. While KANEDA may disclose detecting a horizontal clock signal CPH, it does not disclose detecting that either a video signal or a vertical synchronization signal is no longer being input to the display device. The Official Action acknowledges that YASUI et al. do not disclose detecting the absence of video or sync signal.

In view of the present amendment and the foregoing remarks, it is believed that the present application has been placed in condition for allowance. Reconsideration and allowance are respectfully requested.

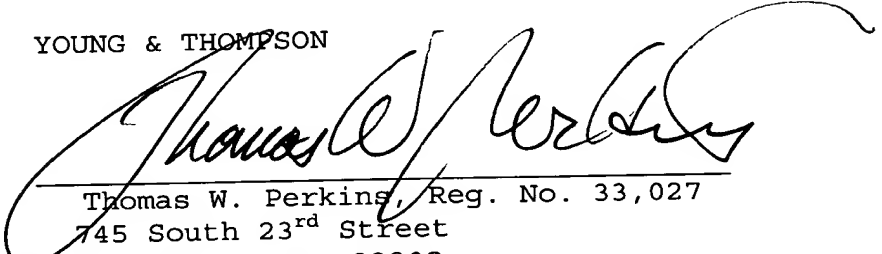
The Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any

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overpayment to Deposit Account No. 25-0120 for any additional
fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17.

Respectfully submitted,

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